THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1967 BY DIGITAL EQUIPMENT CORPORATION ≶RI ≶R2 ≶R3 ≶R6 ≶R8 ≶R9 ≨RII ≶RI2 **≨RI3** RI4 ≶RI9 **▼**D8 **₩**DII **D**13 **V**D21 **D28 ₹**D23 **D38** A DI **∡**D5 **T**D7 **T**DIS **▲** D3 **∡**DI0 DI4 A D20 A D22 **▲**D24 AD26 **₹** D29 ДD31 **▲**D33 \$ R7 ≷ RIO ≶ 1,500 \$R15 \$1,500 \$R20 \$1,500 } i,500 D18 D662 D37 D662 - DI7 D 36 D 662 A D662 D16 D662 D35 D662 D27 D662 D9 D662 六の 六04 D15 D662 D34 D662 OC,V GND UNLESS OTHERWISE INDICATED: RESISTORS ARE 1/4 W, 5% RESISTORS ARE 7,500 CAPACITORS ARE OI MFD DIODES ARE D664 USE THE ETCH BOARD OF THE G796 DATE 7-24-67 TRANSISTOR & DIODE CONVERSION CHART DRN. M. Waller CHK'D. 2 mA LEVEL TERMINATOR G704 DEQUIPMENT SIZE COOF G704-0-1 DEC EIA DEC IN645 IN3606 AYNARD, MASSACHUSETTS PRINTED CIRCUIT REV. A B